#16

FORM PTO-1449

FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.	APPLICATION NO.
1875.0590009	09/766,048
APPLICANT: Carr, *et al.*	
FILING DATE: January 19, 2001	GROUP: 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DC	AA1	1,342,885	06/1920	Armstrong			
DC	AB1	1,773,980	08/1930	Farnsworth			
DC	AC1	2,650,265	08/1953	Mountjoy			
DC	AD1	3,939,429	02/1976	Löhn *et al.*			
DC	AE1	4,027,242	05/1977	Yamanaka			
DC	AF1	4,061,980	12/1977	Sato			
DC	AG1	4,097,899	06/1978	Yu			
DC	AH1	4,139,866	02/1979	Wegner			
DC	AI1	4,162,452	07/1979	Ash			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
DC	AJ1	43 17 220 A1	05/1993	DE	H04N	5/46	Yes (AO26)
DC	AK1	43 21 565 A1	06/1993	DE	H03H	11/48	No
DC	AL1	0 305 602 A1	03/1989	EP	H03D	7/16	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DC	AM	1	*Complaint And Demand For Jury Trial*, 5 Pages, Filed July 15, 2002 in Broadcom Corporation v. Microtune, Inc. *et al.*, U.S. District Court for the Eastern District of Texas- Sherman Division, Case No. 4:02-CV-205.
DC	AN	1	*Defendant Microtune's Original Answer and Counterclaim; Demand for Jury Trial*, 12 Pages, Filed August 5, 2002 in Broadcom Corporation v. Microtune, Inc. *et al.*, U.S. District Court for the Eastern District of Texas-Sherman Division, Case No. 4:02-CV-205.
DC	AO	1	*Broadcom's Motion to Dismiss Microtune's Counterclaim of Patent Unenforceability and to Strike Microtune's Affirmative Defenses of Inequitable Conduct, Patent Misuse, and Unclean Hands*, 14 Pages, Filed August 26, 2002 in Broadcom Corporation v. Microtune, Inc. *et al.*, U.S. District Court for the Eastern District of Texas-Sherman Division, Case No. 4:02-CV-205.
DC	AP	1	Crols, J. and Steyaert, M., "A Fully Integrated 900MHz CMOS Double Quadrature Downconverter," *1995 IEEE International Solid-State Circuits Conference*, IEEE, pp. 136-137 (1995).
DC	AQ	1	Crols, J. and Steyaert, M., "A Single-Chip 900 MHz CMOS Receiver Front-End with a High Performance Low-IF Topology," *IEEE Journal of Solid-State Circuits*, Vol. 30, No. 12, IEEE, pp. 1483-1492 (December 1995).
DC	AR	1	Crols, J. and Steyaert, M., "A Full CMOS 1.5 GHz Highly Linear Broadband Downconversion Mixer," pp. 248-251.

EXAMINER	DATE CONSIDERED
DLC 9L	3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2	4,169,659	10/1979	Marlowe			
	AB2	4,282,549	08/1981	Balaban *et al.*			
	AC2	4,305,053	12/1981	Naitoh *et al.*			
	AD2	4,321,565	03/1982	Ward			
	AE2	4,322,856	03/1982	Ohta *et al.*			
	AF2	4,352,209	09/1982	Ma			
	AG2	4,353,132	10/1982	Saitoh *et al.*			
	AH2	4,395,777	07/1983	Oki *et al.*			
	AI2	4,402,089	08/1983	Knight *et al.*			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ2	0 406 851 A2	01/1991	EP	H04N	5/50	N/A
	AK2	0 469 898 A2	02/1992	EP	H03J	3/28	N/A
	AL2	0 502 449 A1	09/1992	EP	H03D	7/16	No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	2	Wilson, J.F. *et al.*, "A Single-Chip VHF and UHF Receiver for Radio Paging," *IEEE Journal of Solid-State Circuits*, Vol. 26, No. 12, IEEE, pp. 1944-1950 (December 1991).
	AN	2	Thomas, V. *et al.*, "A One-Chip 2GHz Single Superhet Receiver for 2Mb/s FSK Radio Communication," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 42-43 (1994).
	AO	2	Abidi, A.A., "Radio-Frequency Integrated Circuits for Portable Communications," *Custom IC Conference, San Diego, CA*, pp. 151-158 (May 1994).
	AP	2	Long, J.R. and Copeland, M.A., "The Modeling, Characterization, and Design of Monolithic Inductors for Silicon RF IC's," *IEEE Journal of Solid-State Circuits*, Vol. 32, No. 3, IEEE, pp. 357-369 (March 1997).
	AQ	2	Van Dooremolen, W.H.A. and Hufschmidt, M., *Application Note: AN192: A complete FM radio on a chip*, Philips Semiconductors, 14 pages (December 1991).
	AR	2	Meyer, R.G. and Mack, W.D., "A 1-GHz BiCMOS RF Front-End IC," *IEEE Journal of Solid-State Circuits*, Vol. 29, No. 3, IEEE, pp. 350-355 (March 1994).

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DLC	3/04

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA3	4,419,770	12/1983	Yagi *et al.*			
	AB3	4,467,272	08/1984	Hassler *et al.*			
	AC3	4,496,979	01/1985	Yu *et al.*			
	AD3	4,499,602	02/1985	Hermeling, Jr. *et al.*			
	AE3	4,520,507	05/1985	Moon			
	AF3	4,553,264	11/1985	Hasegawa *et al.*			
	AG3	4,554,584	11/1985	Elam *et al.*			
	AH3	4,555,809	11/1985	Carlson			
	AI3	4,569,084	02/1986	Takahama			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ3	0 545 342 A1	06/1993	EP	H04B	1/26	N/A
	AK3	2 586 872 A1	03/1987	FR	H03D	7/00	Yes (AM26)
	AL3	2 120 478 A	11/1983	GB	H03L	7/18	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	3	Dautriche, P. *et al.*, "GaAs Monolithic Circuits for TV Tuners," *GaAS IC Symposium*, IEEE, pp. 165-168 (November 12-14, 1985).
	AN	3	Rauscher, C., "Distributed Microwave Active Filters with GaAs FETs," *IEEE MTT-S Digest*, IEEE, pp. 273-276 (1985).
	AO	3	Driscoll, M.M. *et al.*, "UHF Film Resonator Evaluation and Resonator-Controlled Oscillator and Filter Design Using Computer-Aided Design Techniques," *IEEE MTT-S Digest*, IEEE, pp. 239-242 (1985).
	AP	3	Abidi, A.A., "Low-Power Radio-Frequency IC's for Portable Communications," *Proceedings of the IEEE*, Vol. 83, No. 4, IEEE, pp. 544-569 (April 1995).
	AQ	3	Benson, K.B., *Television Engineering Handbook*, McGraw-Hill Book Company, pp. 13.5 and 13.73 (1986).
	AR	3	Bonek, E. *et al.*, "Personal Communications Transceiver Architectures for Monolithic Integration," *5th IEEE International Symposium on Personal, Indoor and Mobile Radio Communications Proceedings*, IEEE, pp. 363-368 (September 18-22, 1994).

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4	4,580,289	04/1986	Enderby			
	AB4	4,584,715	04/1986	Baars *et al.*			
	AC4	4,614,925	09/1986	Kane			
	AD4	4,619,001	10/1986	Kane			
	AE4	4,627,100	12/1986	Takano			
	AF4	4,631,603	12/1986	Ryan			
	AG4	4,633,315	12/1986	Kasperkovitz			
	AH4	4,661,995	04/1987	Kashiwagi			
	AI4	4,688,263	08/1987	Aldridge			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ4	2 143 691 A	02/1985	GB	H03D	7/16	N/A
	AK4	2 168 864 A	06/1986	GB	H03D	7/00	N/A
	AL4	2 170 368 A	07/1986	GB	H04B	1/30	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	4	Couch II, L.W., *Digital and Analog Communication Systems*, Macmillan Publishing Co. Inc., pp. 62, 63 and 301-304 (1983).
	AN	4	Dautriche, P. *et al.*, "VHF-UHF GaAs Monolithic Front End," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 216-217 (1987).
	AO	4	Fenk, J. and Täuber, R., "TV VHF/Hyperband Tuner ICs," *IEEE Transactions on Consumer Electronics*, Vol. CE-32, No. 4, IEEE, pp. 723-733 (November 1986).
	AP	4	Gilbert, B., *Design Considerations for BJT Active Mixers*, Analog Devices Inc., 58 pages (1994).
	AQ	4	Henderson, B.C. and Cook, J.A., "Image-Reject and Single-Sideband Mixers," *WJ Tech Notes*, Vol. 12, No. 3, Watkins-Johnson Company, pp. 1-13 (May/June 1985).
	AR	4	Kasperkovitz, W.G., "FM receivers for mono and stereo on a single chip," *Philips Technical Review*, Vol. 41, No. 6, pp. 169-182 (1983/1984).

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DN	AA5	4,689,740	08/1987	Moelands *et al.*			
DN	AB5	4,739,390	04/1988	Achiha *et al.*			
DN	AC5	4,742,566	05/1988	Nordholt *et al.*			
DN	AD5	4,745,478	05/1988	Nakagawa *et al.*			
DN	AE5	4,776,040	10/1988	Ichikawa *et al.*			
DN	AF5	4,812,772	03/1989	Hatfield			
DN	AG5	4,818,903	04/1989	Kawano			
DN	AH5	4,876,737	10/1989	Woodworth *et al.*			
DN	AI5	4,879,758	11/1989	DeLuca *et al.*			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
DN	AJ5	2 171 570 A	08/1986	GB	H03D	7/16	N/A
DN	AK5	2 183 970 A	06/1987	GB	H04B	1/02	N/A
DN	AL5	2 215 565 A	09/1989	GB	H04B	1/10	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	5	Khorramabadi, H., *High-Frequency CMOS Continuous Time Filters*, Dissertation, University of California, Berkeley, pp. i-iv and 1-140 (1985).
DN	AN	5	Koullias, I.A. *et al.*, "A 900MHz Transceiver Chip Set for Dual-Mode Cellular Radio Mobile Terminals," *IEEE International Solid-State Circuits Conference*, pp. 140-141 (1993).
DN	AO	5	Kurpis, G.P. *The New IEEE Standard Dictionary of Electrical and Electronics Terms: Fifth Edition*, IEEE, pp. 1347 and 1420 (January 15, 1993).
DN	AP	5	Lakshmikumar, K.R. *et al.*, "A Baseband Codec for Digital Cellular Telephony," *IEEE Journal of Solid-State Circuits*, Vol. 26, No. 12, IEEE, pp. 1951-1958 (December 1991).;
DN	AQ	5	Larson, L.E., "An Improved GaAs MESFET Equivalent Circuit Model for Analog Integrated Circuit Applications," *IEEE Journal of Solid-State Circuits*, Vol. SC-22, No. 4, pp. 567-574 (August 1987).
DN	AR	5	Longo, L. *et al.*, "A Cellular Analog Front End with a 98dB IF Receiver," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 36-37 (1994).

EXAMINER DLC	DATE CONSIDERED 3/04

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA6	4,885,802	12/1989	Ragan			
	AB6	4,918,532	04/1990	O'Connor			
	AC6	4,918,748	04/1990	Shahriary *et al.*			
	AD6	5,010,400	04/1991	Oto			
	AE6	5,014,349	05/1991	Kubo *et al.*			
	AF6	5,020,147	05/1991	Okanobu			
	AG6	5,028,887	07/1991	Gilmore			
	AH6	5,049,831	09/1991	Westwick			
	AI6	5,060,297	10/1991	Ma *et al.*			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ6	2 218 871 A	11/1989	GB	H03D	7/18	N/A
	AK6	2 223 900 A	04/1990	GB	H04B	1/26	N/A
	AL6	2 224 912 A	05/1990	GB	H04B	1/26	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	6	Makram-Ebeid, S. and Minondo, P., "The Roles of the Surface and Bulk of the Semi-Insulating Substrate in Low-Frequency Anomalies of GaAs Integrated Circuits," *IEEE Transactions on Electron Devices*, Vol. ED-32, No. 3, IEEE, pp. 632-642 (March 1985).
	AN	6	Marshall, C. *et al.*, "A 2.7V GSM Transceiver ICs with On-Chip Filtering," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 148-149 (1995).
	AO	6	McDonald, M.D., "A 2.5GHz BiCMOS Image-Reject Front-End," *International Solid-State Circuits Conference*, IEEE, pp. 144, 145 and 279 (1993).
	AP	6	Mills, T.B. and Suzuki, H.S., "Design Concepts for Low-Cost Transistor AGC Systems," pp. 38-43.
	AQ	6	Moon, T.H., "A High Performance VHF Solid-State TV Tuner," pp. 209-219.
	AR	6	Nabe-Yama, H. and Miyazaki, G., "An AGC System Design Based Upon the DC Restoration and the Dynamic Characteristics," pp. 329-338 (September 24, 1970).

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U.S. PATENT DOCUMENTS

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	AA7	5,093,922	03/1992	Kubo *et al.*			
	AB7	5,142,369	08/1992	Grubbs *et al.*			
	AC7	5,142,370	08/1992	Wignot *et al.*			
	AD7	5,142,371	08/1992	Lehmann			
	AE7	5,144,439	09/1992	Wignot			
	AF7	5,144,440	09/1992	Wignot *et al.*			
	AG7	5,146,337	09/1992	Grubbs			
	AH7	5,146,338	09/1992	Lehmann *et al.*			
	AI7	5,148,280	09/1992	Wignot *et al.*			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ7	2 236 225 A	03/1991	GB	H03D	7/18	N/A
	AK7	2 242 588 A	10/1991	GB	H04B	1/30	N/A
	AL7	2 250 877 A	06/1992	GB	H03J	5/00	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	7	Okanobu, T. *et al.*, "A New Radio Receiver System for Personal Communications," IEEE, pp. 334-335 (1995).
	AN	7	Okanobu, T. and Tomiyama, H., "An Advanced Low Power Radio 1 Chip IC," *International Conference on Consumer Electronics Digest of Technical Papers*, IEEE, pp. 242-243 (1994).
	AO	7	Okanobu, T. and Yamazaki, D., "Single Chip Radio IC Including Synchronous Detector," *IEEE Transactions on Computer Electronics*, Vol. 38, No. 3, IEEE, pp. 476-481 (August 1992).
	AP	7	Pavio, A.M. and Halladay, R.H., "A Distributed Double-Balanced Dual-Gate FET Mixer," *GaAs IC Symposium*, IEEE, pp. 177-180 (1988).
	AQ	7	Poch, W.J. and Epstein, D.W., "Partial Suppression of One Side Band in Television Reception," *Proceedings of the Institute of Radio Engineers*, Vol. 25, No. 1, Part 1, pp. 15-31 (January 1937).
	AR	7	Scheinberg, N. *et al.*, "A Low-Frequency GaAs MESFET Circuit Model," *IEEE Journal of Solid-State Circuits*, Vol. 23, No. 2, IEEE, pp. 605-608 (April 1988).

EXAMINER	DATE CONSIDERED
	8/04

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA8	5,179,729	01/1993	Mishima *et al.*			
	AB8	5,203,032	04/1993	Usui			
	AC8	5,212,817	05/1993	Atkinson			
	AD8	5,212,824	05/1993	Mishima *et al.*			
	AE8	5,265,267	11/1993	Martin *et al.*			
	AF8	5,280,639	01/1994	Kubo			
	AG8	5,317,216	05/1994	Hosoya *et al.*			
	AH8	5,355,524	10/1994	Higgins, Jr.			
	AI8	5,355,532	10/1994	Kubo *et al.*			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ8	2 282 286 A	03/1995	GB	H04B	1/00	N/A
	AK8	58-70614	04/1983	JP	H03H	9/64	Yes (AN26)
	AL8	1-273432	11/1989	JP	H04B	1/16	No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	8	Sevenhans, J. *et al.*, "An Analog Radio Front-end Chip Set for a 1.9GHz Mobile Radio Telephone Application," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 44-45 (1994).
	AN	8	Steyaert, M., "RF Integrated Circuits in Standard CMOS Technologies," pp. 1-8.
	AO	8	Thomas, V. *et al.*, "A One-Chip 2GHz Single Superhet Receiver for 2Mb/s FSK Radio Communication," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 42-43 (1994).
	AP	8	Van Dooremolen, W.H.A. and Hufschmidt, M., "A complete f.m. radio on a chip," *Electronic Components and Applications*, Vol. 5, No. 3, pp. 159-170 (June 1983).
	AQ	8	Yamamoto, A. *et al.*, "A Compact Satellite 1 GHz Tuner With GaAs ICs," *IEEE Transactions on Consumer Electronics*, Vol. 35, No. 3, IEEE, pp. 397-405 (August 1989).
	AR	8	Yamazaki, D. *et al.*, "A Complete Single Chip AM Stereo/FM Stereo Radio IC," *IEEE Transactions on Consumer Electronics*, IEEE, Vol. 40, No. 3, pp. 563-569 (August 1994).

EXAMINER	DATE CONSIDERED
D. LC	5/6/4

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	AA9	5,361,406	11/1994	Wignot *et al.*			
	AB9	5,361,407	11/1994	Sawada *et al.*			
	AC9	5,365,551	11/1994	Snodgrass *et al.*			
	AD9	5,369,440	11/1994	Sussman			
	AE9	5,392,011	02/1995	Li			
	AF9	5,398,080	03/1995	Sakashita *et al.*			
	AG9	5,410,270	04/1995	Rybicki *et al.*			
	AH9	5,410,735	04/1995	Borchardt *et al.*			
	AI9	5,420,646	05/1995	Dobrovolny			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ9	4-340803	11/1992	JP	H03D	7/18	No
	AK9	8-130690	05/1996	JP	H04N	5/46	No
	AL9	WO 89/06072 A1	06/1989	PCT	H04B	1/26	No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	9	Yamazaki, D. *et al.*, "A Complete Single Chip AM Stereo/FM Stereo Radio IC," *International Conference on Consumer Electronics Digest of Technical Papers*, IEEE, pp. 244-245 (1994).
	AN	9	Murakami, T. and Sonnenfeldt, R.W., "Detection of Asymmetric Sideband Signals in the Presence of Noise," *IRE Transactions on Broadcast and Television Receivers (Reprinted from RCA Review)*, pp. 46-75 (September 1958).
	AO	9	Goldman, S., "Television Detail and Selective-Sideband Transmission," *Proceedings of the I.R.E.*, pp. 725-732 (November 1939).
	AP	9	Loughlin, B.D., "Color Signal Distortions in Envelope Type of Second Detectors," *IRE Transactions on Broadcast and Television Receivers*, pp. 81-93 (1957).
	AQ	9	Eckersley, P.P., "Asymmetric-Side-Band Broadcasting," *Proceedings of the Institute of Radio Engineers*, Vol. 26, No. 9, pp. 1041-1092 (September 1938).
	AR	9	Hollywood, J.M., "Single-Sideband Filter Theory with Television Applications," *Proceedings of the I.R.E.*, pp. 457-472 (July 1939).

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	AA10	5,428,836	06/1995	Sanecki *et al.*			
	AB10	5,479,449	12/1995	Patel *et al.*			
	AC10	5,493,210	02/1996	Orndorff *et al.*			
	AD10	5,500,650	03/1996	Snodgrass *et al.*			
	AE10	5,568,512	10/1996	Rotzoll			
	AF10	5,583,850	12/1996	Snodgrass *et al.*			
	AG10	5,584,066	12/1996	Okanobu			
	AH10	5,625,325	04/1997	Rotzoll *et al.*			
	AI10	5,627,544	05/1997	Snodgrass *et al.*			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ10	WO 94/29948 A1	12/1994	PCT	H03D	3/00	N/A
	AK10						Yes No
	AL10						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	10	Brownlee, P., "Running the MC44802 PLL Circuit," 19 Pages.
	AN	10	Nyquist, H. and Pfleger, K.W., "Effect of the Quadrature Component in Single Sideband Transmission," *Bell System Technical Journal*, pp. 63-73.
	AO	10	Taeuber, R. and Fenk, J., "VHF Tuner IC for Use in Television Receivers and CATV Receivers," *IEEE Transactions on Consumer Electronics*, Vol. CE-28, No. 4, IEEE, pp. 508-518 (November 1982).
	AP	10	*ACD0900S3C VHF/UHF CATV/TV Tuner Downconverter Advanced Product Information Rev. 7*, Anadigics, 5 Pages (April 22, 1998).
	AQ	10	*Anadigics Technical Brief: Upconverter MMIC for CATV Preliminary*, Anadigics, 12 pages (January 11, 1994).
	AR	10	*ACU50751 CATV/TV/Cable Modem Upconverter MMIC Advanced Product Information Rev. 1*, Anadigics, 5 Pages (April 22, 1998).

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA11	5,633,927	05/1997	Ryan *et al.*			
	AB11	5,640,213	06/1997	Miyahara *et al.*			
	AC11	5,648,744	07/1997	Prakash *et al.*			
	AD11	5,686,864	11/1997	Martin *et al.*			
	AE11	5,715,282	02/1998	Mansouri *et al.*			
	AF11	5,717,718	02/1998	Rowsell *et al.*			
	AG11	5,717,730	02/1998	Prakash *et al.*			
	AH11	5,722,040	02/1998	Bjerede *et al.*			
	AI11	5,739,730	04/1998	Rotzoll			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ11						Yes No
	AK11						Yes No
	AL11						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	11	*Data Sheet: SA602A Double-balanced mixer and oscillator*, Product Specification, Philips Semiconductors, 11 Pages (November 7, 1997).
	AN	11	*Data Sheet: TDA9887 I²C-bus controlled multistandard alignment-free IF-PLL with FM radio*, Preliminary Specification, Philips Semiconductors, pp. 1, 2, 5, 6 and 43-46 (December 15, 1999).
	AO	11	*Data Sheet: TDA7040T Low voltage PLL stereo decoder*, Product Specification, Philips Semiconductors, 11 Pages (September 1986).
	AP	11	*I²C bus specification*, Philips Semiconductors, pp. 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25 and 27.
	AQ	11	*ACU50751 CATV/TV/Cable Modem Upconverter MMIC Advanced Product Information Rev. 1*, Anadigics, 6 Pages (February 25, 1999).
	AR	11	*ACU50752 CATV/TV/Cable Modem Upconverter MMIC Advanced Product Information Rev. 0*, Anadigics, 5 Pages (September 8, 1998).

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DJC	3/24

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
DN	AA12	5,784,523	07/1998	Quan *et al.*			
DN	AB12	5,790,946	08/1998	Rotzoll			
DN	AC12	5,805,988	09/1998	Clayton *et al.*			
DN	AD12	5,828,589	10/1998	Degenhardt			
DN	AE12	5,914,630	06/1999	Peterson			
DN	AF12	5,920,241	07/1999	Mazzochette			
DN	AG12	5,930,696	07/1999	Tzuang *et al.*			
DN	AH12	5,949,295	09/1999	Schmidt			
DN	AI12	5,953,417	09/1999	Quan			

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ12						Yes No
	AK12						Yes No
	AL12						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	12	"New zero IF chipset from Philips," *Electronic Engineering*, Vol. 67, No. 825, Miller Freeman plc, p. 10 (September 1995).
DN	AN	12	Sandys, J., "NavCore V - an OEM component for a satellite based global positioning and navigation system," *Electronic Engineering*, Vol. 64, No. 786, Morgan-Grampian plc, pp.41, 42, 44 and 46 (June 1992).
DN	AO	12	McDonald, M., "A transceiver chip set for DECT," *Electronic Engineering*, Vol. 64, No. 786, Morgan-Grampian plc, pp. 81, 83 and 84 (June 1992).
DN	AP	12	Banks, D.K. *et al.*, "The INMARSAT Second Generation Communications Payload," pp.781-788.
DN	AQ	12	Imai, K., "A 22GHz Band Low Noise Down Converter for Satellite Broadcast Receivers," pp. 549-554.
DN	AR	12	Johnson, P., "Saws and superhets," *Filters*, 2 pages.

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DLC	5/6/04

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA13	6,160,571	12/2000	Wang			
	AB13	6,169,569,B1	01/2001	Widmer *et al.*			
	AC13	6,308,056 B1	10/2001	Abe *et al.*			
	AD13	6,377,315 B1	04/2002	Carr *et al.*			
	AE13	09/438,688		Vorenkamp *et al.*			11/12/1999
	AF13						
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ13						Yes No
	AK13						Yes No
	AL13						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	13	"Applications Summary: Dual-conversion f.m. receiver/ Intelligent modem," Vol. 95, No. 1637, p. 271 (March 1989).
DN	AN	13	Kraus, K., "Circuit Ideas: Digitally-programmable filter functions," Vol. 96, No. 1638, p. 359 (April 1989).
DN	AO	13	*Television Engineering*, pp. 312-315.
DN	AP	13	Narayanan, S., "Application of Volterra Series to Intermodulation Distortion Analysis of Transistor Feedback Amplifiers," *IEEE Transactions on Circuit Theory*, Vol. CT-17, No. 4, IEEE, pp. 518-527 (November 1970).
DN	AQ	13	Scheinberg, N. *et al.*, "A GaAs Up Converter Integrated Circuit for a Double Conversion Cable TV 'Set-Top' Tuner," *IEEE Journal of Solid-State Circuits*, Vol. 29, No. 6, IEEE, pp. 688-692 (June 1994).
DN	AR	13	Pache, D. *et al.*, "An improved 3V 2GHz BICMOS Image Reject Mixer IC," *Integrated Circuits Conference*, IEEE, pp. 95-98 (1995).

EXAMINER	DATE CONSIDERED
DLC	8/04

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA14						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ14						Yes No
	AK14						Yes No
	AL14						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	14	*Data Sheet: Bipolar Analog Integrated Circuits μPC2711TB, μPC2712TB*, NEC Corporation, 2 Pages (April 1997).
DN	AN	14	*Surface Mount RF Schottky Barrier Diodes Technical Data*, Hewlett Packard, 2 Pages (June 1999).
DN	AO	14	*Type B4F/B4FL Balun Transformers for Surface Mounting*, Toko, 1 Page.
DN	AP	14	"Phono Plugs and Jacks," *Mouser Electronics Catalog*, No. 601, p. 112 (February-April 2000).
DN	AQ	14	*NPN Silicon High Frequency Transistor: NE856 Series*, NEC, 4 Pages (September 1998).
DN	AR	14	Leffel, M., "Intermodulation Distortation in a Multi-Signal Environment," *RF Design*, pp. 78-84 (June 1995).

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D. Le	3/04

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA15						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ15						Yes No
	AK15						Yes No
	AL15						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	15	Jordana, J., "Comments on IMD Article," *RF Design*, p. 15 (March 1996).
	AN	15	Long, J.R. *et al.*, "A Low-Voltage Silicon Bipolar RF Front-End for PCN Receive Applications," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 140-141 (1995).
	AO	15	Long, J.R. *et al.*, "A Low-Voltage Silicon Bipolar RF Front-End for PCN Receiver Applications," *IEEE ISSCC Slide Supplement*, IEEE, pp. 104-105 (1995).
	AP	15	Lovelace, D. *et al.*, "Silicon Upconverter RF IC Simplifies Cable Modem Designs," *Microwaves & RF*, pp. 136, 137, 139, 140 and 142 (March 1997).
	AQ	15	Lovelace, D. *et al.*, "Innovative Simulator Models Silicon Upconverter RF IC," *Microwaves & RF*, pp. 106, 108 and 109 (April 1997).
	AR	15	*CATV Test Equipment Catalog*, Matrix Test Equipment Incorporated, 15 Pages.

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA16						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ16						Yes No
	AK16						Yes No
	AL16						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DA	AM	16	"Analog and Mixed Analog-Digital Layout," *Analog VLSI Signal and Information Processing*, pp. 699-726.
DA	AN	16	*Data Sheet: BC849; BC850 NPN general purpose transistors*, Product Specification, Philips Semiconductors, 8 Pages (April 8, 1999).
DA	AO	16	Bult, K., "Analog Broadband Communication Circuits in Pure Digital Deep Sub-Micron CMOS," *IEEE International Solid-State Circuits Conference*, IEEE, pp. 76-77 (1999).
DA	AP	16	Farmer, J.A. "Specifications for Tuner Design for Use in Cable Ready Television Receivers and VCRs," *IEEE Transactions on Consumer Electronics*, Vol. 36, No. 3, IEEE, pp. 660-668 (August 1990).
DA	AQ	16	*SAW Components: IF Filter for Intercarrier Applications*, Siemens Matsushita Components, 4 Pages (January 1, 1998).
DA	AR	16	*Bandpass Filter*, Siemens Matsushita Components, pp. 2-7 (January 1996).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA17						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ17						Yes No
	AK17						Yes No
	AL17						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	17	*LMV321 Single/ LMV358 Dual/ LMV324 Quad General Purpose, Low Voltage, Rail-to-Rail Output Operational Amplifiers*, National Semiconductor Corporation, 10 Pages (1999).
DN	AN	17	*+2.5V to +5.5V, 120 µA, 2-Wire Interface, Voltage Output 8-/10-/12-Bit DACs: AD5301/AD5311/AD5321*, Analog Devices, Inc., pp. 1, 4, 5 and 15 (1999).
DN	AO	17	*DCK (R-PDSO-G5) Mechanical Data*, Texas Instruments, 2 Pages (June 1999).
DN	AP	17	*General Purpose Transistor: NPN Silicon*, Motorola, pp. 1, 7 and 8 (1996).
DN	AQ	17	CSM-7 SMD Quartz Crystal, *SMD Crystals*, ECS International, Inc., p. 18.
DN	AR	17	*PIC16C63A/65B/73B/74B: 8-bit CMOS Microcontrollers with A/D Converter*, Microchip Technology Inc., 3 Pages (1999).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA18						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ18						Yes No
	AK18						Yes No
	AL18						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	18	*AH2 High Dynamic Range Amplifier*, Rev. 2.0, WJ Wireless Products Group, 2 pages (September 1998).
DN	AN	18	*NPN 9 Ghz wideband transistor BFR540*, Philips Semiconductors, pp. 2 and 11 (August 23, 1999).
DN	AO	18	*NPN general purpose transistors BC846; BC847; BC848*, Philips Semiconductors, p. 2 (March 12, 1997).
DN	AP	18	*Silicon PIN Diodes: BAR 14-1...BAR16-1*, Infineon Technologies, pp. 1-3 (October 4, 1999).
DN	AQ	18	*Surface Mount EMI Filters Chip Filters NFM51R Series*, Murata Electronics, p. 192.
DN	AR	18	Simons, K.A., "The Decibel Relationships Between Amplifier Distortion Products," *Proceedings of the IEEE*, Vol. 58, No. 7, IEEE, pp. 1071-1086 (July 1970).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1565.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA19						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ19						Yes No
	AK19						Yes No
	AL19						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	19	Jones, B.L., "Subjective Assessment of Cable Impairments on Television Picture Quality," *NCTA- Technical Papers*, pp. 1-25 (1992).
DN	AN	19	*Microtuner™ 2000 Product Brief: MT2000, MT2000PAL, MT2000N/P*, Microtune, pp. 1-4 (July 1999).
DN	AO	19	*Microtuner 2003 Product Brief: MT2003, MT2003PAL, MT2003N/P*, Microtune, pp. 1-5 (December 1999).
DN	AP	19	Mensink, C.H.J. *et al.*, "A CMOS 'Soft-Switched' Transconductor and Its Application in Gain Control and Filters," *IEEE Journal of Solid-State Circuits*, Vol. 32, No. 7, IEEE, pp. 989-998 (July 1997).
DN	AQ	19	*The LM1823: A High Quality TV Video I.F. Amplifier and Synchronous Detector for Cable Receivers*, National Semiconductor Corporation, pp. 1-16 (March 1985).
DN	AR	19	*Operation and Programming Manual: HP 8561A abd HP8562A/B Portable Spectrum Analyzers*, Hewlett-Packard Company, Figure E-1 (July 1989).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA20						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ20						Yes No
	AK20						Yes No
	AL20						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	20	Scheinberg, N. *et al.*, "A GaAs Up Converter Integrated Circuit for a Double Conversion Cable TV 'Set-Top' Tuner," *International Conference on Consumer Electronics Digest of Technical Papers*, IEEE, pp. 108-109 (June 8-10, 1993).
DN	AN	20	Torii, K. *et al.*, "Monolithic Integrated VHF TV Tuner," *IEEE Transactions on Consumer Electronics*, Vol. CE-26, IEEE, pp. 180-189 (May 1980).
DN	AO	20	Ablassmeier, U. *et al.*, "GaAs FET Upconverter for TV Tuner," *IEEE Transactions on Electron Devices*, Vol. ED-27, No. 6, IEEE, pp. 1156-1159 (June 1980).
DN	AP	20	Shreve, W.R. and Stigall, R.E., "Surface Acoustic Wave Devices for Use in a High Performance Television Tuner," *IEEE Transactions on Consumer Electronics*, Vol. CE-24, No. 1, IEEE, pp. 96-104 (February 1978).
DN	AQ	20	Archer, J.W. *et al.*, "A Broad-Band UHF Mixer Exhibiting High Image Rejection over a Multidecade Baseband Frequency Range," *IEEE Journal of Solid-State Circuits*, Vol. SC-16, No. 4, IEEE, pp. 385-392 (August 1981).
DN	AR	20	Putnam, J. and Puente, R., "A Monolithic Image-Rejection Mixer on GaAs Using Lumped Elements," *Microwave Journal*, pp. 107, 108, 110, 114 and 116 (November 1987).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY DOCKET NO. 187.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA21						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ21						Yes No
	AK21						Yes No
	AL21						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	21	Taddiken, B. *et al.*, "Broadband Tuner on a Chip for Cable Modem, HDTV, and Legacy Analog Standards (Invited)," *IEEE Radio Frequency Integrated Circuits Symposium*, IEEE, pp. 17-20 (June 11-13, 2000).
DN	AN	21	*Secure Signals*, National Cable Television Association, pp. 1-7 (August 1995).
DN	AO	21	*From The Source*, Vol. 8, No. 1, National Cable Television Association, pp. 1-10 (November 1995).
DN	AP	21	*NFM2012P13C105F*, Murata Manufacturing Co., Ltd., 5 Pages (1999).
DN	AQ	21	*Mouser Stock No. 163-5003, 163-5004*, Mouser Electronics, 1 Page (January 4, 2000).
DN	AR	21	*Surface Mount EMI Filters: Chip Filters NFM□□P Series*, Murata Electronics, pp. 186-187.

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DLC	3/04

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1815.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA22						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ22						Yes No
	AK22						Yes No
	AL22						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	22	*Surface Mount EMI Filters: Soldering*, Murata Electronics, p. 204.
DN	AN	22	*SS22 thru SS26: Surface Mount Schottky Rectifiers*, General Semiconductor, 2 Pages (January 11, 2001).
DN	AO	22	*Aluminum Electrolytic Capacitors*, 2 Pages.
DN	AP	22	*Type 594D Solid Tantalum Chip Capacitors*, Vishay Sprague, pp. 59-63.
DN	AQ	22	"4.0 Receiver Performance," Carl T. Jones Corporation, pp. 4.1-4.84.
DN	AR	22	Boutin, N., "Complex Signals: Part 1," *RF Design*, pp. 27-33 (December 1989).

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA23						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ23						Yes No
	AK23						Yes No
	AL23						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DC	AM	23	Boutin, N., "Complex Signals: Part II," *RF Design*, pp. 57-60 and 62-65 (January 1990).
DC	AN	23	Boutin, N., "Complex Signals: Part III," *RF Design*, pp. 109-111 and 113-115 (March 1990).
DC	AO	23	Boutin, N., "Complex Signals: Part IV," *RF Design*, pp. 65-70 and 73-75 (May 1990).
DC	AP	23	*LM117/LM317A/LM317 3-Terminal Adjustable Regulator*, National Semiconductor Corporation, pp. 1-26 (August 1999).
DC	AQ	23	*Wideband RF Transformers*, Mini-Circuits Distribution Centers, 3 Pages.
DC	AR	23	*Decoder Interface Standard Draft IS-105.1*, Rev. 6.0, Decoder Interface Subcommittee in conjunction with Working Party 9 - Drafting, pp. i and 1-26 (March 12, 1996).

EXAMINER DC	DATE CONSIDERED 3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA24						
	AB24						
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	AI24						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ24						Yes No
	AK24						Yes No
	AL24						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DLC	AM	24	*EIA Interim Standard: Immunity of Television Receivers and Video Cassette Recorders (VCR's) to Direct Radiation From Radio Transmissions, 0.5 to 30 MHz*, Electronic Industries Association Engineering Department, pp. i and 1-10 (May 1987).
DLC	AN	24	*EIA Interim Standard: Recommended Design Guideline Rejection of Educational FM Interference to CH 6 Television Reception*, Electronic Industries Association Engineering Department, pp. 1-19 (July 1987).
DLC	AO	24	*Data Sheet: TDA6402; TDA6403: 5 V mixer/oscillator and synthesizer for cable TV and VCR 2-band tuners*, Preliminary Specification, Philips Semiconductors, 37 Pages (October 15, 1996).
DLC	AP	24	Gharpurey, R. and Meyer, R.G., "Modeling and Analysis of Substrate Coupling in Integrated Circuits," *IEEE Journal of Solid-State Circuits*, Vol. 31, No. 3, IEEE, pp. 344-353 (March 1996).
DLC	AQ	24	*Tuners*, TEMIC Microsystems, 6 Pages.
DLC	AR	24	*Data Sheet: FI1236MK2: Desktop video tuner system RTMA M/N*, Preliminary Specification, Philips Semiconductors, 20 Pages (March 1, 1996).

EXAMINER DLC	DATE CONSIDERED 3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1836.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA25						
	AB25						
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	AH25						
	AI25						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ25						Yes No
	AK25						Yes No
	AL25						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DN	AM	25	*Data Sheet: TDA5737M: Low power VHF, UHF and hyperband mixer/oscillator for TV and VCR 3-band tuners* Product Specification, Philips Semiconductors, 20 Pages (March 21, 1995).
DN	AN	25	*Data Sheet: TDA5636; TDA5637: 9 V VHF, hyperband and UHF mixers-oscillators for TV and VCR 3-band tuners*, Product Specification, Philips Semiconductors, 28 Pages (June 10, 1996).
DN	AO	25	*Data Sheet: FR1236: Desktop video & radio module system RTMN M/N*, Preliminary Specification, Philips Semiconductors, 20 Pages (March 1, 1996).
DN	AP	25	*Philips Components Desktop Video Tuner Specification FI1236 (System M/N)*, Philips Display Components Company, 31 Pages (September 1993).
DN	AQ	25	Burghartz, J.N. *et al.*, "High-Q Inductors in Standard Silicon Interconnect Technology and its Application to an Integrated RF Power Amplifier," *IEDM*, pp. 1015-1017 (1995).
DN	AR	25	Craninckx, J. and Steyaert, M.S.J., "A 1.8-Ghz Low-Phase-Noise CMOS VCO Using Optimized Hollow Spiral Inductors," *IEEE Journal of Solid-State Circuits*, Vol. 32, No. 5, IEEE, pp. 736-744 (May 1997).

EXAMINER	DATE CONSIDERED
DLC 9/6	3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY DOCKET NO. 187-10590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA26						
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	AI26						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ26						Yes No
	AK26						Yes No
	AL26						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DJ	AM	26	Translation of French Patent Publication No. 2 586 872 A1, prepared by Ralph McElroy Translation Company, 12 Pages (March 6, 1987 - Date of publication of application).
DJ	AN	26	Translation of Japanese Patent Publication No. 58-70614, prepared by Ralph McElroy Translation Company, 6 Pages (April 27, 1983 - Date of publication of application).
DJ	AO	26	Translation of German Patent Publication No. 43 17 220 A1, prepared by Ralph McElroy Translation Company, 9 Pages (December 1, 1994 - Date of publication of application).
DJ	AP	26	English-language Abstract of Japanese Patent Publication No. 01-273432, 2 Pages (November 1, 1989 - Date of publication of application).
DJ	AQ	26	English-language Abstract of Japanese Patent Publication No. 04-340803, 2 Pages (November 27, 1992 - Date of publication of application).
DJ	AR	26	English-language Abstract of Japanese Patent Publication No. 08-130690, 2 Pages (May 21, 1996 - Date of publication of application).

EXAMINER DJC	DATE CONSIDERED 3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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FORM PTO-1449 FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANT Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA27						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ27						Yes No
	AK27						Yes No
	AL27						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DV	AM	27	DIALOG File 348 (European Patents) English-Language Patent Abstract for European Patent Publication No. 502449, 2 pages (September 9, 1992 - Date of publication of application).
DV	AN	27	DIALOG File 349 (PCT Fulltext) English-Language Patent Abstract for PCT International Patent Publication No. WO 8906072, 2 pages (June 29, 1982 - Date of publication of application).
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	AQ	27	
	AR	27	

EXAMINER DLC	DATE CONSIDERED 3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

#19

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.	APPLICATION NO.
1875.0590009	09/766,048
APPLICANTS: Carr *et al.*	
FILING DATE: January 19, 2001	GROUP: 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA46						
	AB46						
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	AH46						
	AI46						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ46						Yes No
	AK46						Yes No
	AL46						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	46	
	AN	46	*CXA1951AQ GPS Down Converter*, Sony Corporation, pages 1-16.
	AO	46	*GSM Receiver Circuit*, Siemens, PMB 2402, Semiconductor Group, pages 1-21.
	AP	46	*HD155111F RF Single-Chip Linear IC For PCN Cellular Systems*, ADE-207-257 (Z), 1st Edition, August, 1998, Renesas Technology Corporation, pages 1-57.
	AQ	46	*HD155121F RF Transceiver IC for GSM and PCN Dual Band Cellular Systems*, ADE-207-265A (Z) 2nd Edition, May, 1999, Renesas Technology Corporation, pages 1-57.
	AR	46	*TRF1020 GSM Receiver EVM*, Texas Instruments, Application Brief SWRA018, Digital Signal Processing Solutions, September 22, 1998, pages 1-25.

EXAMINER	DATE CONSIDERED
DLC	3/6/4

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1875.0590009	APPLICATION NO. 09/766,048
	APPLICANTS Carr *et al.*	
	FILING DATE January 19, 2001	GROUP 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA47						
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	AH47						
	AI47						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ47						Yes No
	AK47						Yes No
	AL47						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	47	*TUA 4300 G*, Siemens AG, IC-Specification, HL DCK, V66047-S695-G100-G1, December 11, 1996, pages 2-39.
	AN	47	Abidi, *Low-Power Radio-Frequency IC's for Portable Communications*, Proceedings of the IEEE, Vol. 83, No. 4, April, 1995, pages 544-569.
	AO	47	Badura, *New Integrated Circuits for GSM Mobile Radio*, Siemens Components XXVI (1991) No. 2, Engineering Integrated Circuits, pages 72-76.
	AP	47	Cathelin *et al.*, *A Fully Integrated CMOS PM Radio Receiver for Wristwatch Calibration*, IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July, 1998, pages 1014-1022.
	AQ	47	Durec, *An Integrated Silicon Bipolar Receiver Subsystem for 900-MHz ISM Band Applications*, IEEE Journal of Solid-State Circuits, Vol. 33, No. 9, September, 1998, pages 1352-1372.
	AR	47	*GSM Receiver Circuit*, Siemens, PMB 2401, Semiconductor Group, pages 1-23.

EXAMINER	DATE CONSIDERED
	5/07

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.	APPLICATION NO.
1875.0590009	09/766,048
APPLICANTS: Carr *et al.*	
FILING DATE: January 19, 2001	GROUP: 2614

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA48						
	AB48						
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	AI48						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ48						Yes No
	AK48						Yes No
	AL48						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

DC	AM	48	Koullias *et al.*, *A 900MHz Transceiver Chip Set fo Dual-Mode Cellular Radio Mobile Terminals*, 1993 IEEE International Solid-State Circuits Conference, ISSCC 93, Session 9, Radio Communication Circuits, Paper TP 9.2, pages 140-141 and 278.
DC	AN	48	McCullagh, *A Single-Chip Silicon Bipolar Receiver for GPS/GLONASS Applications*, IEEE International Solid-State Circuits Conference 1999, Communications Session, pages 1-14.
DC	AO	48	Murphy *et al.*, *A Low-Power, Low-Cost Bipolar GPS Receiver Chip*, IEEE Journal of Solid-State Circuits, Vol. 32, No. 4, April, 1997, pages 587-591.
DC	AP	48	Razavi, *A 900MHz/1.8GHz CMOS Receiver for Dual Band Applications*, IEEE 1998, pages 8.2-1 thru 8.2-8.
DC	AQ	48	Stetzler *et al.*, *A 2.7-4.5 V Single Chip GSM Transceiver RF Integrated Circuit*, IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December, 1995, pages 1421-1429.
DC	AR	48	Veit *et al.*, *A 2.7V 800MHz-2.1GHz Transceiver Chipset for Mobile Radio Applications in 25GHz f_t Si-Bipolar*, 1994 Bipolar/BiCMOS Circuits & Technology Meeting 11.2, 1994 IEEE, pages 175-178.

EXAMINER	DATE CONSIDERED
DC	3/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

37 C.F.R. § 1.501 INFORMATION DISCLOSURE CITATION IN A PATENT	ATTY. DOCKET NO. 1875.0590008	PATENT NO. 6,377,315 B1
	APPLICANTS Carr *et al.*	
	ISSUE DATE April 23, 2002	GROUP ART UNIT NO.

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA49						
	AB49						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ49						Yes No
	AK49						Yes No
	AL49						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AM	49	Yamawaki *et al.*, *A 2.7-V GSM RF Transceiver IC*, IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December, 1997, pages 2089-2096.
	AN	49	
	AO	49	
	AP	49	
	AQ	49	
	AR	49	

EXAMINER	DATE CONSIDERED
	7/07

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.